

PART B - FEE(S) TRANSMITTAL

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30425 7590 02/27/2004

STMICROELECTRONICS, INC.
 MAIL STATION 2346
 1310 ELECTRONICS DRIVE
 CARROLLTON, TX 75006



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I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO, on the date indicated below.

(Depositor's name)
Angie Rodriguez
(Signature)
May 10, 2004
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/800,039

03/06/2001

Pervez Hassan Sagarwala

93-C-091C1

7946

(STMI01-00012)

TITLE OF INVENTION: CMOS INTEGRATED CIRCUIT DEVICE WITH LDD N-CHANNEL TRANSISTOR AND NON-LDD P-CHANNEL TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

NO

\$1330

\$300

\$1630

05/27/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
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PRENTY, MARK V

2822

257-369000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Lisa K. Jorgenson

2 William A. Munck-

3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

STMicroelectronics, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Carrollton, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

☒ Issue Fee☒ Publication Fee☒ Advance Order - # of Copies 10

4b. Payment of Fee(s):

☒ A check in the amount of the fee(s) is enclosed.☐ Payment by credit card. Form PTO-2038 is attached.

☒ The Director is hereby authorized by charge the required fee(s), or credit any overpayment Deposit Account Number 19-1353 (enclose an extra copy of this form).

Director for Patents is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

(Authorized Signature)

(Date)

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Alexandria, Virginia 22313-1450.

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05/17/2004 FNETEKI2 00000143 09800039

01 FC:1501

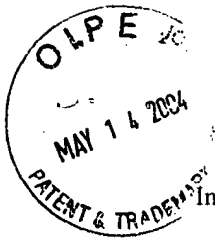
1330.00 OP

02 FC:1504

300.00 OP

03 FC:8001

30.00 OP



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Applicant :	Pervez Hassan Sagarwala	Docket No :	93-C-91D1
Serial No :	09/800,039	Art Unit :	2822
Filed :	March 6, 2001	Examiner :	Mark V. Prenty
For :	CMOS Integrated Circuit Device With LDD N-Channel Transistor And Non- LDD P-Channel Transistor	Confirmation No :	7946

TRANSMITTAL LETTER

Mail Stop Issue Fee
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowance and Fee(s) Due mailed February 27, 2004, for the above-identified patent application, enclosed are the following:

- (1) Part B-Fee(s) Transmittal PTOL-85 with Certificate of Mailing.
- (2) Check in the amount of \$1,660.00 for payment of Issue Fee; and advanced order of ten copies.
- (3) Our return postcard.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Mario J. Donato, Jr.
Reg. No. 37,816
Attorney for Applicant

STMicroelectronics, Inc.
1310 Electronics Drive/MS 2346
Carrollton, TX 75006
972-466-7503

CERTIFICATE OF MAILING
37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail with sufficient postage, in an enveloped addressed to: Mail Stop Issue Fee, Commissioner for Patents, P. O. Box 1450 Alexandria, VA. 22313-1450 on the date below:

May 10, 2004
Date

Ange Rodriguez
Signature